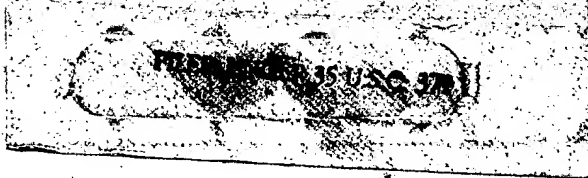


09/720953

	Subclass	ISSUE CLASSIFICATION
	Class	



PATENT NUMBER

## U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED <i>SS</i> O.A. <i>AA</i>	

APPLICATION NO. 09/720953	CONT/PRIOR D F	CLASS <del>364</del> 174	SUBCLASS 262	ART UNIT <del>2835</del> 2844	EXAMINER NORRIS
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APPLICANTS  
Kiyotaka Tsukada  
Masaru Takada  
Kenji Chihara

TITLE

Printed wiring board and method for producing the same

PTO-2040  
12/99

## ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)					
INTERNATIONAL CLASSIFICATION								

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
				Amount Due	Date Paid
	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	

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